Name Answers

CE-1901-11 - Dr. Durant - Quiz 6 Winter 2016-'17, Week 7 Quiz

- 1. (6 points) Delays and Glitches
- + dras the circult a. (2 points) **Derive** the minimized SOP expression for F(ABCD) = $\Sigma_m(3,4,5,7,11,15)$.
 - b. (1 point) what path(s) of gates is involved in the contamination delay of this circuit? (illustrate in part a)
 - c. (1 point) whot path(s) of gates is involved in the propagation delay of this circuit? (illustrate in part a)
 - d. (1 point) Determine between which pair(s) of minterms a (1-bit input change) glitch may occur.
 - e. (1 point) *Modify* the expression for F to avoid all glitches.







a) 5 + 7 © F= ĀBĒ+ ABD, redundant, nedundant, inimates glitch

2. (4 points) Write the logic equation for the 8:1 MUX, y = f(s,d), where s and d are 0-based vectors of appropriate length. Remember that there is a product term for each of the data terms; this product term checks all values of s to make sure they match the term's corresponding s-minterm.



Name anaurers

CE-1901-12 - Dr. Durant - Quiz 6 Winter 2016-'17, Week 7 Quiz

- (6 points) Delays and Glitches 1.
 - a. (2 points) **Derive** the minimized SOP expression for $F(ABCD) = \Sigma_m(0,1,4,5,12,13,14,15)$. b. (1 point) What path(s) of gates is involved in the contamination delay of this circuit?
 - (illustrate in part a) 6c. (1 point) what path(s) of gates is involved in the propagation delay of this circuit? (illustrate
 - in part a)
 - d. (1 point) **Determine** between which pair(s) of minterms a (1-bit input change) glitch may occur.
 - e. (1 point) Modify the expression for F to avoid all glitches.



2. (4 points) *Write* the logic equation for the 8:1 MUX, y = f(s,d), where s and d are 0-based vectors of appropriate length. Remember that there is a product term for each of the data terms; this product term checks all values of s to make sure they match the term's corresponding s-minterm.

y= 525,5, do + 525, 500, + 525,500, +525,500, +525,5003 + 525,5004 + 525,500 5+ 525,5006 + 525,5007